

### **REMARKS**

Claims 1-34 have been canceled and claims 35-68 have been added. Claims 35-68 are pending in this application, with claims 35, 55, 61 and 65 being the independent claims.

While the Applicant submits that the previous pending claims were patentable over the cited references in order to expedite the prosecution of the application the Applicant has cancelled the previously pending claims. The previous rejections are moot in view of the cancellation of the rejected claims.

The newly added independent claims provide clarity in regard to the aggregate per egress port counts that are maintained. It is submitted that none of the cited references disclose or suggest aggregate counts being maintained as recited. For at least this reason it is submitted that the independent claims are patentable over the cited references. The dependent claims are submitted to be patentable for at least this reason and for the further features recited therein.

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.116

Serial Number: 10/622,285

Filing Date: July 18, 2003

Title: MAINTAINING AGGREGATE DATA COUNTS FOR FLOW-CONTROLLABLE QUEUES

Assignee: Intel Corporation

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Dkt: INT-034

Conclusion

Applicant respectfully submits that the claims are in condition for allowance and notification to that effect is earnestly requested. The Examiner is invited to telephone Applicant's attorney (215-230-5511) to facilitate prosecution of this application.

Respectfully submitted,

SUBHAJIT DASGUPTA

By their Representatives,

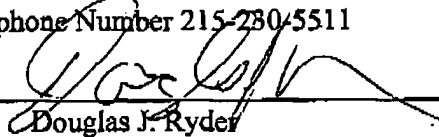
Customer Number 46147

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Date

1/24/06

By



Douglas J. Ryder

Reg. No. 43,073

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 24 day of January, 2006.

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